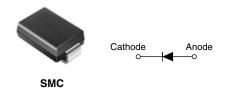
Vishay High Power Products

Schottky Rectifier, 3.0 A



PRODUCT SUMMARY		
I _{F(AV)}	3.0 A	
V _R	40 V	

FEATURES

- Small foot print, surface mountable
- Very low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 30BQ040PbF surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	3.0	А		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	2000	А		
V _F	3.0 Apk, T _J = 125 °C	0.43	V		
TJ	Range	- 55 to 150	°C		

VOLTAGE RATINGS				
PARAMETER	SYMBOL	30BQ040PbF	UNITS	
Maximum DC reverse voltage	V _R	40	V	
Maximum working peak reverse voltage	V _{RWM}	40	v	

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
		50 % duty cycle at T_L = 118 °C, rectangular waveform		3.0	
Maximum average forward current	cimum average forward current I _{F(AV)}		50 % duty cycle at T _L = 110 °C, rectangular waveform		
Maximum peak one cycle non-repetitive surge current		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	2000	A
	IFSM	10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	110	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.0 A, L = 12 mH		6.0	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1.0	А

* Pb containing terminations are not RoHS compliant, exemptions may apply





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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop	V _{FM} ⁽¹⁾	3 A	T _J = 25 °C	0.53	v
		6 A		0.68	
		3 A	T _J = 125 °C	0.43	
		6 A		0.57	
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V_{R} = Rated V_{R}	0.5	mA
		T _J = 125 °C		30	
Maximum junction capacitance	CT	V_{R} = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		230	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		3.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		- 55 to 150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾		12	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}	DC operation	46	
Approximate weight			0.24	g
		0.008	oz.	
Marking device		Case style SMC (similar to DO-214AB)	Va	F

Notes

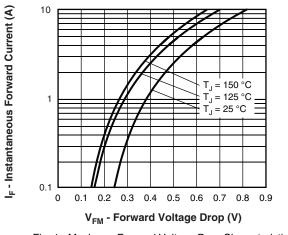
 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

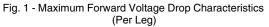
(2) Mounted 1" square PCB

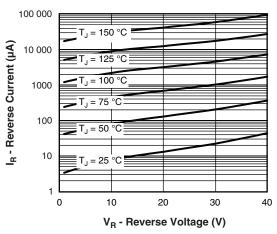


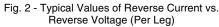
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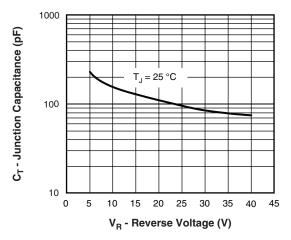


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

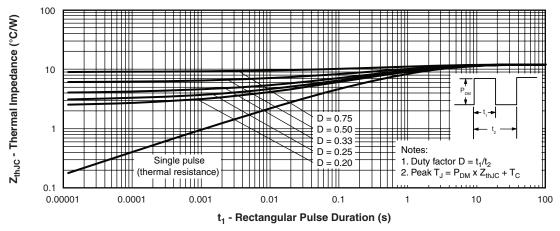
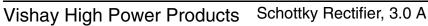
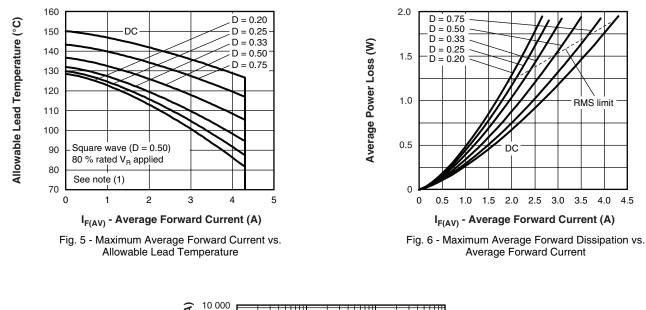


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

30BQ040PbF





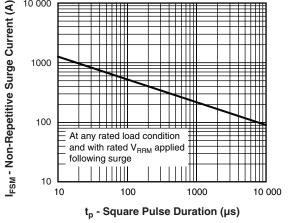


Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$; $Pd = Forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$ (see fig. 6); $Pd_{REV} = Inverse power loss = V_{R1} \times I_R (1 D)$; $I_R at V_{R1} = 80 \%$ rated V_R

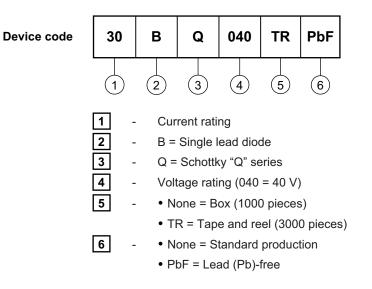
ISHA



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ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
Dimensions	http://www.vishay.com/doc?95023		
Part marking information	http://www.vishay.com/doc?95029		
Packaging information	http://www.vishay.com/doc?95034		
SPICE model	http://www.vishay.com/doc?95324		



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